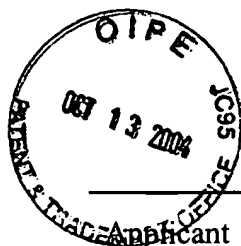


OCT 14 2004

IFW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Brian A. Franchuk

Appln. No. : 10/781,372

Filed : February 18, 2004

Title : SYSTEM AND METHOD FOR
ASSOCIATING A DLPDU RECEIVED BY
AN INTERFACE CHIP WITH A DATA
MEASUREMENT MADE BY AN
EXTERNAL CIRCUIT

Group Art Unit: 2133

Examiner:

Docket No. : E252.12-0005

LETTER

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SENT VIA EXPRESS MAIL
Express Mail No.: EV 485710287 US

This is to inform the Patent and Trademark Office of the following co-pending patent applications that have disclosures related to the above-identified patent application:

1) U.S. Pat. App. Ser. No. 10/781,374 filed on February 18, 2004 for "SYSTEM AND METHOD FOR MAINTAINING A COMMON SENSE OF TIME ON A NETWORK SEGMENT" by Brian A. Franchuk et al.;

2) U.S. Pat. App. Ser. No. 10/903,317 filed on July 30, 2004 for "COMMUNICATION CONTROLLER WITH AUTOMATIC TIME STAMPING" by Brian A. Franchuk et al.;

3) U.S. Pat. App. Ser. No. 10/903,399 filed on July 30, 2004 for "SYSTEM AND METHOD FOR PREVENTING TRANSMISSION DURING MESSAGE RECEPTION" by Brian A. Franchuk et al.;

4) U.S. Pat. App. Ser. No. 10/903,698 filed on July 30, 2004 for "COMMUNICATION CONTROLLER FOR COORDINATING TRANSMISSION OF SCHEDULED AND UNSCHEDULED MESSAGES" by Brian A. Franchuk et al.;

5) U.S. Pat. App. Ser. No. 10/936,142 filed on September 8, 2004 for "DISCARDING

Inventor: Brian A. Franchuk

Serial No.: 10/781,372

-2-

A PARTIALLY RECEIVED MESSAGE FROM A DATA QUEUE” by Brian A. Franchuk et al.;

6) U.S. Pat. App. Ser. No. 10/936,421 filed on September 8, 2004 for “LOW LATENCY DATA PACKET RECEPTION AND PROCESSING” by Brian A. Franchuk et al.; and

7) U.S. Pat. App. Ser. No. 10/936,629 filed on September 8, 2004 for “MANAGEMENT OF EVENT ORDER OF OCCURRENCE ON A NETWORK” by Brian A. Franchuk et al.

Respectfully submitted,

KINNEY & LANGE, P.A.

Date:

10/13/04

By:



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